IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Chung-Hui Chen Examiner: Unknown

Serial No.: 10/784,705 Group Art Unit: Unknown

Filed: February 23, 2004 Attorney Docket No.: N1280-00100

PROGRAMMABLE MOS DEVICE FORMED BY STRESSING For:

POLYCRYSTALLINE SILICON

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

This Information Disclosure Statement is being filed in connection with the patent application referenced above. The Examiner is requested to consider each of the references cited herein and forward an initialed copy of the enclosed PTO Form 1449 to the undersigned representative. This Statement is being filed:

		Upon	filing the application.						
Within three months from the filing date of this application, which is r continued prosecution application.									
		Within	Vithin three months from date of entry of the national stage for this application.						
		Before	re the mailing date of a first Office action on the merits for this application.						
contin	ued exa	Before the mailing date of a first Office action after the filing of a request for mination.							
	ance, anompanie	nd (iii) a	e the mailing date of any of: (i) a Final Office Action, (ii) a Notice of any other action that closes prosecution of this application. The Statement						
			A Statement pursuant to 37 C.F.R. §1.97(e) or						
			The fee set forth in 37 C.F.R. §1.17(p), believed to be						
			Before payment of the Issue Fee. The Statement is accompanied by:						
			A Statement pursuant to 37 C.F.R. §1.97(e) and						
			The fee set forth in 37 C.F.R. §1.17(p), believed to be						

I.	Provis	ion of Copies
enclos	⊠ ed here	A copy of each of the references listed on the attached Form PTO Form 1449 is with and forms a part hereof.
part he	creof.	A translation of References AA, AB and AC are enclosed herewith and form a
applica	□ ation is	A copy of the European Search Report from a corresponding or related EPO enclosed herewith.
applica	□ ation is	A copy of the International Search Report from a corresponding or related PCT enclosed herewith.
Applic submit	cation Nated to c	This application claims priority, pursuant to 35 U.S.C. §120, to prior U.S. No, filed References not submitted herewith were recited by the Office in the prior application(s).
II.	Fee	
	nis com	No fee is believed due in connection with filing of this Statement. However, in see or refund is due, the Commissioner is authorized to charge any fee associated munication or credit any overpayment to Deposit Account No. 04-1679 (N1280-
		The fee in the amount \$ that is due in connection of filing of this s paid upon electronic filing of a corresponding Statement, which was made on sing the ePAVE software.
to be d	ue in co	The fee set forth in 37 C.F.R. §1.17(p), believed to be \$, is believed onnection of filing of this Statement.
		A check in the amount \$ is enclosed.

Respectfully submitted,

The Commissioner is authorized to charge any other fee associated with this

Dated: 4218 2014

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(415) 371-2201 (Fax)

communication, or credit any overpayment, to Deposit Account No. 04-1679 (N1280-00100).

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Form PTO-	1449 M6d	HESTOEMAN	Docket No. N1280-00100(TSMC2003-841)			Serial No. Unassigned		
List of Pate Cited by Ap (Use several	nt and Pu	blications	Applicant Chung-Hui Chen et al.					
U.S. Depart Patent and	ment of C	ommerce	Filing Date 02/23/2004			Group Unknown		
U. S. PATE	NT DOCU	UMENTS						
Examiner Initial	Cite No.	Document No.	Kind Code (if known)	Name		Date	Class	Subclass
	1.	6,507,087 B		Yu		01/14/2003	257/529	
	2.	6,445,606 B		Khoury		09/03/2002	365/96	
	3.	6,436,738 B	1	Yu		08/20/2002	438/132	
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	Cite	1					Page,	
Examiner Initial	No.	Country Number Kind Code Code		Publication Date		me of Patentee Applicant	Columns, Lines where Relevant Passages Appeal	
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OTHER PR	IOR ART	- NON PAT	ENT LITERATURE	DOCUMENTS				
Examiner Initial	Cite No.	(Include name of author in CAPITAL LETTERS, Title of the Article (when appropriate) title of the item (book, magazine, journal, symposium, catalog, etc.) date, pages, volume, issue number(s), publisher, city and/or country where published)						
	5.	Ker, Ming-Dou et al., "CAPACITOR-COUPLE ESD PROTECTION CIRCUIT FOR DEEP-SUBMICRON LOW-VOLTAGE CMOS ASIC", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 4, No. 3, (SEPT. 1996), pp. 307-321						
	6.	Ker, Ming-Dou et al., "COMPLEMENTARY-SCR ESD PROTECTION CIRCUIT WITH INTERDIGITATED FINGER-TYPE LAYOUT FOR INPUT PADS OF SUBMICRON CMOS IC'S", IEEE Transactions on Electron Devices, Vol. 42, No. 7, (JUL 1995), pp. 1297-1304						

EXAMINER	DATE CONSIDERED

Wu et al., A NEW ON-CHIP ESD PROTECTION CIRCUIT WITH DUAL PARASITIC

SCR STRUCTURES FOR CMOS VLSI, IEEE Journal of Solid-State Circuits, Vol. 27, No.

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

7.

3, (MAR 1992), pp. 274-280